



JFW

Brian D. Ogonowsky

Carmen C. Cook

David C. Hsia

Rachel V. Leiterman

April 4, 2005

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Thomas S. Wong, Robert W. Bechdolt, Phi Thai
Assignee: Micrel, Inc.
Title: Integrated Schottky Transistor Logic Configuration
Serial No.: 10/665,645
Examiner: Vibol Tan
Docket No.: MIC-M082
Filed: September 18, 2003
Group Art Unit: 2819

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) This Transmittal Letter (1 page);
- (2) Response to Office Action (7 pages);
- (3) Replacement Drawings (Figs. 3, 5, and 6) (1 sheet).

- ☒ No additional fee is required.
☐ The fee has been calculated as shown below:

	Claims Remaining After Amendment		Highest No. Previously Paid For		Present Extra	Rate	Additional Fee
Total Claims	18	Minus	20	=	0	x \$50	\$ 0.00
Independent Claims	2	Minus	3	=	0	x \$200	\$ 0.00
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application						\$
<input checked="" type="checkbox"/>	Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.						
<input type="checkbox"/>	Please charge our Deposit Account No. 502226 in the amount of						0.00
<input checked="" type="checkbox"/>	Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 502226.						
Total:						\$	0.00

I hereby certify that this correspondence is being deposited with
the United States Postal Service as First Class Mail in an envelope
addressed to: Mail Stop Amendment, Commissioner for Patents,
P. O. Box 1450, Alexandria, VA 22313-1450, on the date
shown below.

Attorney for Applicant(s)

4/4/05
Date

Respectfully submitted,

Brian D. Ogonowsky
Attorney for Applicant(s)
Reg. No. 31,988



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Thomas S. Wong, Robert W. Bechdolt, Phi Thai
Assignee: Micrel, Incorporated
Title: Integrated Schottky Transistor Logic Configuration
Serial No.: 10/665,645 Filing Date: September 18, 2003
Examiner: Vibol Tan Group Art Unit: 2819
Docket No.: MIC-M082

San Jose, California
April 4, 2005

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P. O. BOX 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

This responds to the office action dated January 4, 2005.

Amendments to the claims begin on page 2. The claims are generally being merged, their dependencies changed, or clarified.

Remarks begin on page 6.

Patent Law Group LLP
635 N. First St. Suite 223
San Jose, CA 95134
(408) 382-0480
FAX (408) 382-0481